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cadence
ACADEMIC NETWORK

3rd ONE WEEK WORKSHOP ON VLSI DEVICE, CIRCUIT AND SYSTEM DESIGN TOOLS (ONLINE)

Organized By : School of Electronics Engineering (SENSE)

Date : 27th June to 3rd July 2023 (Tuesday to Monday)



**E-Certificate
for participation**



Registration link:
<https://vtop1.vitap.ac.in/CSDT/Conferenceinitial>

About Our Workshop

The aim of this workshop is to provide an overview on EDA tools for VLSI design to the participants. The workshop will comprise of live demonstration of tools and lectures delivered by design engineers on platforms like Cadence, Quiskit , MagCAD, Silvaco, TCAD, SCAPS, and Quantum ATK. This workshop is a generous attempt of qualified VLSI domain researchers to help and encourage the research scholars, undergraduate, postgraduate, and entrepreneurs to meet their research goals. The participants will have exposure to the state-of-the-art semiconductor devices to circuit modeling and integrated applications. After finishing this workshop, the participants will be able to execute the various analysis needed to characterize devices and circuits individually.

Who can attend: Faculty/UG/PG/Research scholars/Person from the industry

Platform: Online (Link will be provided after the registration)

Workshop Topics:

- ❖ Material intelligence in neuromorphic AI hardware
- ❖ Spintronic device and circuit simulation tools
- ❖ Scripting for VLSI using cadence tool
- ❖ The Future Electronics Technology Roadmap: Quantum Computing and Architecture Beyond CMOS Using Quiskit and MagCAD
- ❖ Emerging label free biosensors and pH Sensors simulation using Silvaco
- ❖ Modeling and Simulation of Nano-Transistors with open-source and online TCAD tools
- ❖ Design and Modelling of Pb-free perovskite solar cells using SCAPS
- ❖ Structural, Electronic, and Optical properties calculations with Quantum ATK

Registration details and dates:

- Available Seats: 60 (first-come-first-serve basis)
- Last date of application: 23/6/2023
- Intimation of selection: 24/6/2023
- REGISTRATION FEE: INR 590
- REGISTRATION & PAYMENT LINK (For External Participants)

Register through the link given below.

The payment link is available inside the registration form.

<https://vtop1.vitap.ac.in/CSDT/Conferenceinitial>

AGENDA:

Session-I (10 am to 12 noon)

Session-II (02 pm to 05 pm)

**Day-1 Tuesday,
27th June 2023**



Dr. Hirofumi Tanaka,

Professor, Department of Human Intelligent Systems,
Director, Research Center for Neuromorphic AI Hardware,
Kyushu Institute of Technology, Japan



Dr. Shivam Verma

Assistant Professor
Department of Electronics Engineering
IIT BHU Varanasi

**Day-2 Wednesday,
28th June 2023**



Dr. Neeraj Kumar Misra,

Associate Professor,
School of Electronics Engineering,
VIT-AP University, Amravati (AP)



Mr. V.Navaneethakrishnan

Sr. Application Engineer(AE),
Entuple Technologies Pvt. Ltd

**Day-3 Friday,
30th June 2023**



Dr. Manash Chanda

Associate Professor and Head, Dept. of ECE,
Meghnad Saha Institute of Technology, Kolkata
Chairman, IEEE EDS Kolkata Chapter

**Day-4 Saturday,
1st July 2023**



Dr. Prabhat Kumar Dubey,

Post Doctoral Research Associate,
University of Pisa, Italy



Dr. Pankaj Sharma

Assistant Professor Department of ECE IIITDM,
Jabalpur(MP)

**Day-5 Monday,
3rd July 2023**



Dr. Neerja Dharamle

Assistant professor
Dr. Rajendra Gode Institute of Technology
and Research, Amravati

Coordinators:

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