



VIT-AP
UNIVERSITY

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Advanced Digital design using Verilog HDL (A Value Added Course)

offered by

School of Electronics Engineering (SENSE)

Verilog HDL is one of the widely used coding language for designing hardware modules in digital system IC design. It is used by most of the VLSI industries. The current course emphasizes on training students on advanced Verilog HDL coding concepts with example designs and to develop essential skills required to do projects on advanced digital design using Verilog HDL. The course also focuses on developing ability to understand any digital system specification in depth and to design its architecture at system and sub-system level



Course Benefits :-

- * The students will be able to understand Verilog HDL concepts and will be able to apply them to design advanced digital system specifications
- * The students will be able to design advanced arithmetic circuits using Verilog HDL
- * The students will be able to design and model DSP functions and floating point operations using Verilog HDL
- * The students will be able to design neural network components using Verilog HDL

EVERY MONDAY 11.00 AM to 01.00 PM

Commence from 21-02-2022.

Certificate after completion of course

Instructors : Dr. Purnachand Nalluri & Dr. Deepak Kumar Panda